

10/579911
1AP20Rec'd PCT/PTO 19 MAY 2006

XA-10573

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

HANZAWA, Satoru et al.

Intl. Appln. No.: PCT/JP2003/014901

Intl. Filing Date: 21 November 3003

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

* * *

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Attn: Mail Stop PCT, DO/EO/US

Sir:

Prior to examination, please amend the above-identified patent application as indicated below.

Amendments to the claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks begin on page 29 of this paper.